

Substitute for Form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known	
				Application Number	New Application
				Filing Date	Concurrently herewith
				First Named Inventor:	YOUNG et al.
				Art Unit	Not Yet Assigned
				Examiner Name	Not Yet Assigned
				Attorney Docket Number	42P19119
Sheet	1	of	2		

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)				
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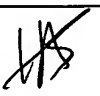

FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No. ¹	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³	Number ⁴	Kind Code ⁵ (if known)				
	1.	WO	01/59895 A1		08-16-2001	GIGATERA AG		

Examiner Signature		Date Considered
		7/3/2006

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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Washington, DC 20231. DO NOT SENT FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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Sheet	2	of	2	Attorney Docket Number	42P19119
NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T ²
	2.	HOOGLAND et al., "Passively Mode-Locked Diode-Pumped Surface-Emitting Semiconductor Laser," <i>IEEE PHOTONICS TECHNOLOGY LETTERS</i> , vol. 12, No. 9, September 2000, pp. 1135-1137			
	3.	KARDOSH, IHAB et al., "Vertical-Extended-Cavity Surface-Emitting Lasers," <i>Annual Report 2002, Optoelectronics Department, University of Ulm</i> , pp. 1-7			
	4.	BROWNELL, MICHAEL, "External Cavity Design Yields Ultrafast VCSEL," <i>spie's oemagazine</i> , May 2002, pp. 10-11,			
	5.	HÄRING, RETO et al., "High-Power Passively Mode-Locked Semiconductor Lasers," <i>IEEE JOURNAL OF QUANTUM ELECTRONICS</i> , vol. 38, No. 9, September 2002, pp. 1268-1275			
	6.	JASIM, K. et al., "Passively Modelocked Vertical Extended Cavity Surface Emitting Diode Laser," <i>ELECTRONICS LETTERS</i> , vol. 39, No. 4, 20 th February 2003, pp. 373-375			
	7.	McINERNEY, J.G. et al., "High-Power Surface Emitting Semiconductor Laser with Extended Vertical Compound Cavity," <i>ELECTRONICS LETTERS</i> , vol. 39, No. 6, 20 th March 2003, pp. 523-525			
	8.	LUTGEN, S. et al., "8-W High-Efficiency Continuous-Wave Semiconductor Disk Laser at 1000 NM," <i>APPLIED PHYSICS LETTERS</i> , vol. 82, No. 21, 26 May 2003, pp. 3620-3622			

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

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Complete if Known

Application Number	10/814,050
Filing Date	March 31, 2004
First Named Inventor:	Young et al.
Art Unit	2872
Examiner Name	Not Yet Assigned
Attorney Docket Number	42P19119

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)				
US XX	1.	US-	5,574,738	11-12-1996	Morgan	
	2.	US-	5,627,853	05-06-1997	Mooradian et al.	
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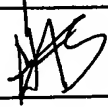
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		Country Code ³	Number ⁴	Kind Code ⁵ (if known)				
XX	3.	EP	0398038	A2	11-22-1990	IBM Corporation		
	4.	JP	2003218449		07-31-2003	Ricoh Co., Ltd.		

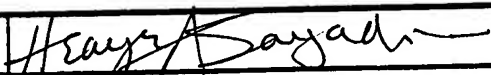
Examiner
Signature*Haye Sayadri*

Date Considered

7/3/2006

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	5.	ARAHIRA, Shin et al., "Mode-Locking at Very High Repetition Rates More than Terahertz in Passively Mode-Locked Distributed-Bragg-Reflector Laser Diodes," IEEE JOURNAL OF QUANTUM ELECTRONICS, Vol. 32, No. 7, July 1996, pp. 1211-1224.			
	6.	KRAINER, L. et al., "77 GHz soliton on modelocked Nd:YVO ₄ laser," ELECTRONICS LETTERS, Vol. 36, No. 22, October 26, 2000.			
	7.	SPÜHLER, G. J. et al., "Output-coupling semiconductor saturable absorber mirror," APPLIED PHYSICS LETTERS, Vol. 78, No. 18, April 30, 2001, pp. 2733-2735.			
	8.	HUANG, Xiaodong et al., "Passive mode-locking in 1.3 μm two-section InAs quantum dot lasers," APPLIED PHYSICS LETTERS, Vol. 78, No. 19, May 7, 2001, pp. 2825-2827.			
	9.	RAFAILOV, E. U. et al., "Fast quantum-dot saturable absorber for passive mode-locking of solid-state lasers," IEEE, 2003, pp. 557-558.			
	10.	GOMES, Luis A. et al., "Picosecond SESAM-Based Ytterbium Mode-Locked Fiber Lasers," IEEE JOURNAL OF SELECTED TOPICS IN QUANTUM ELECTRONICS, Vol. 10, No. 1, January/February 2004, pp. 129-136.			
	11.	LIVERINI, V. et al., "Low-loss GaInNAs saturable absorber mode locking a 1.3-μm solid-state laser," APPLIED PHYSICS LETTERS, Vol. 84, No. 20, May 17, 2004, pp. 4002-4004.			
	12.	LORENSER, D. et al., "Towards wafer-scale integration of high repetition rate passively mode-locked surface-emitting semiconductor lasers," APPLIED PHYSICS B (LASERS AND OPTICS), Vol. B79, No. 8, December 2004, pp. 927-932.			
	13.	PCT Search Report, PCT/US2005/010400, July 6, 2005.			
	14.	PCT Written Opinion, PCT/US2005/010400, July 6, 2005.			

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